

#### **Course Tittle: Electrical Networks Lab**

#### Following documents are available in Course File.

S.No.	Points	Yes	No
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2	PEO & PO Mapping		
3	Academic Calendar		
4	Subject Allocation Sheet		
5	Class Time Table, Individual Timetable (Single Sheet)		
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7	Course Handout		
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9	CO-Cognitive Level Mapping		
10	Lecture Notes		
11	Tutorial Sheets With Solution		
12	Soft Copy of Notes/Ppt/Slides		
13	Sessional Question Paper and Scheme of Evaluation		
14	Best, Average and Weak Answer Scripts for Each Sessional Exam. (Photocopies)		
15	Assignment Questions and Solutions		
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17	Result Analysis		
18	Feedback From Students		
19	Course Exit Survey		
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#### **Course Instructor / Course Coordinator**

#### **Course Instructor / Course Coordinator**

(Name)

(Signature)



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#### **GOKARAJU RANGARAJU** INSTITUTE OF ENGINEERING AND TECHNOLOGY

### Department of Electrical & Electronics Engineering

Institute and Department Vision and Mission Statements

### **Department/Program-EEE**

### **MISSION OF THE INSTITUTE:**

To achieve and impart quality education with an emphasis on practical skills and social relevance.

#### VISION OF THE INSTITUTE:

To be among the best of the institutions for engineers and technologists with attitudes, skills and knowledge and to become an epicenter of creative solutions.

#### VISION OF THE PROGRAM:

To impart technical knowledge and skills required to succeed in life, career and help society to achieve self-sufficiency.

#### MISSION OF THE PROGRAM:

- To become an internationally leading department for higher learning.
- To build upon the culture and values of universal science and contemporary education.
- To be a center of research and education generating knowledge and technologies which lay groundwork in shaping the future in the fields of electrical and electronics engineering.
- To develop partnership with industrial, R&D and government agencies and actively participate in conferences, technical and community activities.





2 PEO & PO Mapping

# Program Educational Objectives (PEO's):

**PEO 1:** Graduates will have a successful technical or professional careers, including supportive and leadership roles on multidisciplinary teams.

**PEO 2:** Graduates will be able to acquire, use and develop skills as required for effective professional practices.

**PEO 3:** Graduates will be able to attain holistic education that is an essential prerequisite for being a responsible member of society.

**PEO 4:** Graduates will be engaged in life-long learning, to remain abreast in their profession and be leaders in our technologically vibrant society.

#### **Program outcomes**

- a) Ability to apply knowledge of mathematics, science, and engineering.
- b) Ability to design and conduct experiments, as well as to analyze and interpret data.
- c) Ability to design a system, component, or process to meet desired needs within realistic

constraints such as economic, environmental, social, political, ethical, health and safety,

manufacturability, and sustainability.

- d) Ability to function on multi-disciplinary teams.
- e) Ability to identify, formulates, and solves engineering problems.
- f) Understanding of professional and ethical responsibility.
- g) Ability to communicate effectively.
- h) Broad education necessary to understand the impact of engineering solutions in a

global, economic, environmental, and societal context.

- i) Recognition of the need for, and an ability to engage in life-long learning.
- j) Knowledge of contemporary issues.
- k) Ability to utilize experimental, statistical and computational methods and tools necessary for engineering practice.
- Graduates will demonstrate an ability to design electrical and electronic circuits, power electronics, power systems; electrical machines analyze and interpret data and also an ability to design digital and analog systems and programming them.

#### PEOs & POs Mapping

Programme Educational Programme Outcomes (POs)
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### Department of Electrical & Electronics Engineering

Objectives (PEOs)	1	2	3	4	5	6	7	8	9	10	11	12
1	Μ	М	-	-	Н	-	-	Η	Н	-	Н	Н
2	-	-	Μ	Μ	Н	Н	Н	-	-	-	-	Н
3	-	-	-	-	Н	Н	Μ	М	М	М	Н	Н
4	-	-	-	Μ	Μ	Н	Μ	Η	Н	-	М	Н

\* H: Strongly Correlating (3); M: Moderately Correlating (2) & L: Weakly Correlating (1)

### **COURSE OBJECTIVES**

Academic Year

### : 2018-2019

Semester

: I

Name of the Program: EEE...... B.Tech ...II/I..... Section: A,B

Course/Subject: Electrical Networks Lab..... Code: GR17A2038

Name of the Faculty: Y.Satyavani

Dept: .....EEE.....

Designation: Assistant professor

On completion of this Subject/Course the student shall be able to:

S.No	Course Objectives
1.	Understand the concept of circuit elements lumped Circuits and various types of sources, V-I
	relation for various input signals and Kirchhoff's Laws and network reduction techniques.
2.	•Understand the concept of alternating quantities, analysis of R, L, C parameters applied with
	ac sinusoidal voltage, concept of reactance, impedance, susceptance and admittance and
	concept of real and reactive powers and power factor.
3.	•Understand network topology a technique used for analyzing and solving electrical
	networks, loop analysis and nodal analysis method, concept of duality and dual networks,
	concept of KVL& KCL.
4.	•Understand concept of resonance, bandwidth, measurement of reactive and active power
	and measurement three phase voltages and currents.
5.	•Learn about Superposition theorem for dc and ac. Excitations, Thevenin's theorem, Norton's
	theorem Maximum power transfer theorem, Compensation theorem for dc and ac excitations.
6.	•Demonstrate networks thermos using components on breadboard.
7.	Learn how to simulate an electrical circuit using electrical softwares Course





### **COURSE OUTCOMES**

Academic Year	: 2018-2019		
Semester	: I		
Name of the Program:	EEE B.Tech	II/I	Section: A,B
Course/Subject: Electrical	letworks Lab	Code: GR1	7A2038
Name of the Faculty: Y.Saty	avani	Dept:	EEE
Designation: Assistant prof	essor		

The expected outcomes of the Course/Subject are:

S.No	Course Outcomes
1.	Understand the knowledge of mathematics, science and engineering.
2.	Identify, formulate and solve engineering problems.
3.	Analyze and design basic lumped circuits.
4.	Participate and try to succeed in competitive examinations.
5.	Simulate network circuits.
6.	Use techniques, skills and modern engineering tools necessary for engineering practice.
7.	Connect hardware components practically on breadboard.

Signature of HOD

Signature of faculty

Date:





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Academic Calendar

### ACADEMIC CALENDAR Academic Year 2018-19

## II B.TECH – FIRST SEMESTER

S. No.	EVENT	PERIOD	DURATION
1	1 <sup>st</sup> Spell of Instructions	02-07-2018 to 05-09-2018	9 Weeks 3 Days
2	1 <sup>st</sup> Mid-term Examinations	06-09-2018 to 08-09-2018	3 Days
3	2 <sup>nd</sup> Spell of Instructions	10-09-2018 to 27-10-2018	7 Weeks
4	2 <sup>nd</sup> Mid-term Examinations	29-10-2018 to 31-10-2018	3 Days
5	Preparation	01-11-2018 to 07-11-2018	1 Week
6	End Semester Examinations (Theory/	08-11-2018 to 08-12-2018	4 Weeks 3 Days
	Practicals) Regular/Supplementary		
7	Commencement of Second Semester,	10-12-2018	
	A.Y 2018-19		

### **II B.TECH – SECOND SEMESTER**

S. No.	EVENT	PERIOD	DURATION
1	1 <sup>st</sup> Spell of Instruction	10-12-2018 to 06-02-2019	8 Weeks 3 days
2	1 <sup>st</sup> Mid-term Examinations	07-02-2019 to 09-02-2019	3 Days
3	2 <sup>nd</sup> Spell of Instruction	11-02-2019 to 03-04-2019	7 Weeks 3 Days
4	2 <sup>nd</sup> Mid-term Examinations	04-04-2019 to 06-04-2019	3 Days
5	Preparation	08-04-2019 to 17-04-2019	1 Week 3 Days
6	End Semester Examinations (Theory/	18-04-2019 to 08-05-2019	3 Weeks
	Practicals) Regular		
7	Supplementary and Summer Vacation	09-05-2019 to 22-06-2019	6 Weeks 3 Days
8	Commencement of First Semester,	24-06-2019	
	A.Y 2019-20		



# Department of Electrical & Electronics Engineering

4 Subject Allocation Sheet

Subject Code	Subject Name	Faculty Code	Faculty Name	Almanac		
GR17A2058	Special Functions and Complex Variable	Dr GS	Dr G Swapna	1 <sup>st</sup> Spell of Instructions	02-07-2018 to 05- 09-2018	
GR17A2076	Computer Organization	PRK	P Ravi Kanth	1 <sup>st</sup> Mid-term Examinations	06-09-2018 to 08- 09-2018	
GR17A2034	Electromagnetic Fields	SN	Syed Sarfaraz Nawaz	2 <sup>nd</sup> Spell of Instructions	10-09-2018 to 27- 10-2018	
GR17A2035	Network Theory	MS	M Srikanth	2 <sup>nd</sup> Mid-term Examinations	29-10-2018 to 31- 10-2018	
GR17A2036	DC Machines and Transformers	Dr BPB	Dr B Phaneendra Babu	Preparation	01-11-2018 to 07- 11-2018	
GR17A2037	DC Machines Lab	DSR/PRK	D Srinivasa Rao/P Ravikanth	End Semester Examinations	08-11-2018 to 08-	
GR17A2038	Electrical Networks Lab	YSV / GBR	Y Satya Vani/ G Bhaskar Rao	(Theory/ Practicals) Regular / Supplementary	12-2018	
GR17A2039	Electrical Simulation Lab	GSR/PS	G Sandhya Rani / P Sirisha	Commencement of	12/10/2018	
GR17A2001	Environmental Science	Bh.SR	Bh. Saroja Rani	Second Semester, A.Y	12/10/2018	



### Department of Electrical & Electronics Engineering

Class Time Table, Individual Timetable (Single Sheet)

#### DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

GRIET/PRIN/06/G/01/18-19

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wef: 02 July 2018

B.Tech - EE	r - I Semester											
Day/Hour	9:00 - 9:45	9:45 - 10:3 0	10:30 - 11:15	11:15- 12:00	12:00 - 12:30	12:30 - 1:20	1:20 - 2:10	2:10 -3:00		Room No.		
MONDAY			b /DCM La A1/A2	b		ES	DCMT	DCMT	Theory	4401		
TUESDAY		-	.ab / EN La A1/A2	b		DCMT	DCMT	NT	Lab	DCM Lab-2106 ES Lab- 4508		
WEDNESDA Y			ıb/ES Lat A1/A2	)	BREAK	NT	NT	со	Lab	ES Lab- 4508 EN Lab- 4510		
THURSDAY	SFC	2V	E	MF	EAK	ES	со	со	Class	V V S Madhuri		
FRIDAY	NT	-	E	MF		SFCV	со	со	Incharge :	v v S Madriun		
SATURDAY	EM	F	D	СМТ		SFCV	SFCV	NT				
Subject Code	Subject Name Faculty Code					Faculty Nam	e		Almanac			
GR17A2058		l Functio plex Va		Dr GS	Dr G Swapna			1 <sup>st</sup> Spell of I	nstructions	02-07-2018 to 05-09- 2018		
GR17A2076	Compu	ter Orga	nization	PRK	P Ravi Kanth			1 <sup>st</sup> Mid-term	Examinations	06-09-2018 to 08-09- 2018		
GR17A2034	Electro	magneti	c Fields	SN	Syed Sarfaraz Nawaz			2 <sup>nd</sup> Spell of	Instructions	10-09-2018 to 27-10- 2018		
GR17A2035	Network Theory MS			MS	M Srika	ikanth 2 <sup>nd</sup> Mid-			n Examinations	29-10-2018 to 31-10- 2018		
GR17A2036	-	Machine: ansform		Dr BPB	Dr B Ph	naneendra Ba	abu	Preparation		01-11-2018 to 07-11- 2018		
GR17A2037	DC Machines Lab DSR/MP			Prasha	D Srinivasa Rao/M Prashanth			ter ns (Theory/	08-11-2018 to 08-12-			
GR17A2038	Electrical Networks Lab YSV / GBR			Y Saty Rao	Y Satya Vani/ G Bhaskar Rao			Regular / tary	2018			
GR17A2039	Electrica	al Simula	ation Lab	GSR/PS	G Sand	lhya Rani / P	Sirisha	Commence		10-12-18		
GR17A2001	Environmental Science MHK				M Harit				nester, A.Y	10-12-10		



# Department of Electrical & Electronics Engineering

#### B.Tech - EEE - B

#### II Year - I Semester

Day/Hour	9:00 - 9:45	9:45 - 10:3 0	10:30 - 11:15	11:15- 12:00	12:00 - 12:30	12:30 - 1:20	1:20 - 2:10	2:10 -3:00			Room No.	
MONDAY	EM	IF	D	СМТ		SFCV	SFCV	ES		Theory	4402	
TUESDAY	DCN	ИТ	E	MF		NT	СО	со		Lab	DCM Lab-2106 ES Lab- 4508	
WEDNESDA Y	N	Г	(	co	BR	SFCV	EMF	EMF		Lub	EN Lab- 4510	
THURSDAY			b /DCM La B1/B2	b	BREAK	SFCV	SFCV	NT		Class		
FRIDAY		-	.ab / EN La B1/B2	b		NT	NT	ES		Incharge :	VVSMadhuri	
SATURDAY			ıb/ES Lat B1/B2	0		со	DCMT	DCMT				
Subject Code	Subject Name Faculty Code					Faculty Nam	е	Almanac				
GR17A2058		al Function Notex Val		Dr GS	Dr G Sv	vapna		1 <sup>st</sup> Spell of I	nstru	uctions	02-07-2018 to 05-09- 2018	
GR17A2076	Compu	iter Orga	nization	PRK	P Ravi	Kanth		1 <sup>st</sup> Mid-term	Exa	minations	06-09-2018 to 08-09- 2018	
GR17A2034	Electro	magneti	c Fields	SN	Syed Sa	/ed Sarfaraz Nawaz		2 <sup>nd</sup> Spell of Instructions		uctions	10-09-2018 to 27-10- 2018	
GR17A2035	Net	work Th	eory	MS	M Srika	nth	2 <sup>nd</sup> Mid-term Examinations			29-10-2018 to 31-10- 2018		
GR17A2036		Machine: ansform		Dr BPB	Dr B Ph	aneendra Ba	abu	Preparation			01-11-2018 to 07-11- 2018	
GR17A2037	DC Machines Lab K			D Sriniv	rasa Rao/P R	avikanth	End Semest Examination		heory/	08-11-2018 to 08-12-		
GR17A2038	Electric	al Netwo	orks Lab	YSV / GBR	Y Saty Rao	a Vani/ G Bh	askar	Practicals) Regular / Supplementary			2018	
GR17A2039	Electrica	al Simula	ation Lab	GSR/PS	G Sand	hya Rani / P	Sirisha	Commencer	men	t of	10.12.19	
GR17A2001	Enviror	nmental	Science	Bh.SR	Bh. Sar	oja Rani		Second Semester, A.Y			10-12-18	

# Individual timetable

Day/Hour	9:00 - 9:45	9:45 - 10:30	10:30 - 11:15	11:15- 12:00	12:00- 12:30	12:30 - 1:20	1:20 - 2:10	2:10 -3:00		Room No.		
MONDAY										Theory	4401	
TUESDAY		EN	Lab A2							Lab	DCM Lab-2106 ES Lab- 4508	
WEDNESDAY		EN	Lab A1		BREAK					Lub	EN Lab- 4510	
THURSDAY					AK				Class Incharge:		V/V/C Medhuri	
FRIDAY		EN	Lab B2								V V S Madhuri	
SATURDAY		EN	Lab B1									



### Department of Electrical & Electronics Engineering

Syllabus Copy

### **Syllabus**

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Course Code: GR17A2038

L:0 T:0 P:2 C:2 63 GR17 Regulations

#### **Contents:**

- 1. Thevenin's Theorem
- 2.Norton's Theorem
- 3.Maximum power Transfer Theorem
- 4. Superposition Theorem and Reciprocity Theorem
- 5.Z and Yparameters.
- 6. Transmission and Hybrid Parameters
- 7. Compensation and Milliman's Theorems
- 8.Series Resonance
- 9.Parallel Resonance
- 10.Locus of Current Vector in an R-LCircuit
- 11.Locus of Current Vector in an R-C Circuit

12. Measurement of 3-phase power by two watt meter method for unbalanced loads



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### Department of Electrical & Electronics Engineering

Course Handout

# **COURSE SCHEDULE**

Academic Year		: 2018-2019		
Semester		: I		
Name of the	Program:	EEE B.Tech	II/I	Section: A,B
Course/Subject:	Electrical Ne	Code: GR1	7A2038	
Name of the Face	ulty: Y.Satya	vani	Dept:	EEE

Designation: Assistant professor The Schedule for the whole Course / Subject is:

			Total No. of
Exp. No.	Description	Duration(Date)	Periods
1.	Thevenin's theorem	09/07/18	4
2.	Norton's Theorem	09/07/18	4
3.	Maximum Power Transfer Theorem	16/07/18	4
4.	Superposition Theorem	16/07/18	4
5.	Reciprocity Theorem	20/07/18	4
6.	Compensation Theorem	23/07/18	4
7	Millman's Theorem	27/07/18	4
8.	Z parameters	30/07/18	4
9.	Y parameters	03/08/18	4
10	Transmission Parameters	17/08/18	4
11.	Hybrid Parameters	24/08/18	4
12.	Series Resonance	31/08/18	4
13.	Parallel Resonance	14/09/18	4
14.	Measurement of 3 phase power by 2 watt meter method for unbalanced loads	21/09/18	4



## Department of Electrical & Electronics Engineering

Total No. of Instructional periods available for the course: .... Hours / Periods

Academic Year		<b>EDULE OF INSTRU</b> : 2018-2019	CTIONSCO	URSE PLAN
Semester		: I		
Name of the	Program:	EEE B.Tech	II/I	Section: A,B
Course/Subject:	Electrical Ne	etworks Lab	Code: GR1	7A2038
Name of the Fact	ulty: Y.Satya	vani	Dept:	EEE

Designation: Assistant professor

Exp .No	Topics	Objectives & Outcome s	References(Text Book, Journal)
			Circuits and Networks by A.SUDHAKAR and
1.	Thevenin's theorem	1,2,3 & 1,2	S.P.SHYAMMOHAN
2.	Norton's Theorem	1,2,3 & 1,2	Circuits and Networks by A.SUDHAKAR and S.P.SHYAMMOHAN
3	Maximum Power Transfer Theorem	1,2,3& 1,2	Circuits and Networks by A.SUDHAKAR and S.P.SHYAMMOHAN
4	Superposition Theorem	1,2,3& 1,2	Circuits and Networks by A.SUDHAKAR and S.P.SHYAMMOHAN
5	Reciprocity Theorem	1,2,3& 1,2	Circuits and Networks by A.SUDHAKAR and S.P.SHYAMMOHAN
6	Compensation Theorem	1,2,3 & 1,2	Circuits and Networks by A.SUDHAKAR and S.P.SHYAMMOHAN
7	Millman's Theorem	1,2,3 & 1,2	Circuits and Networks by A.SUDHAKAR and S.P.SHYAMMOHAN
8	Z and Y parameters	1,2,3 & 2	Circuits and Networks by A.SUDHAKAR and S.P.SHYAMMOHAN
9	Transmission and Hybrid	1,2,3 & 2	Circuits and Networks by A.SUDHAKAR



### Department of Electrical & Electronics Engineering

10	Ser	ies Resonance	Circuits and Networks by A.SUDHAKAR		
11	Par	allel Resonance	1,2,3,4 & 2	Circuits and Networks by A.SUDHAKAR	
12	power by 2 watt meter method		1,2,3,4 ,5,6 & 2	Circuits and Networks by A.SUDHAKAR	
8	3	CO-PO Mapping			

#### Assessment methods:

- 1. Operation skill and familiarization of software.
- 2. Experimental procedure, simulation results, internal observation, lab record.
- 3. Internal examinations.
- 4. External examinations.
- 5. Viva- voce.

# **1.** Course Objectives-Program Outcomes (POs) Relationship Matrix (Indicate the relationships by mark "X")

P-Outcomes	а	В	С	d	е	F	g	h	i	j	k	Ι
C-Objectives												
1	Х	Х	Х	Х	Х				Х	Х	Х	Х
2	Х				Х		Х	Х		Х	Х	
3	Х	Х	Х			Х	Х	Х	Х		Х	Х
4				Х	Х	Х		Х	Х	Х	Х	
5		Х	Х	Х					Х	Х		
6				Х	Х	Х		Х		Х	Х	
7	Х	X	Х	Х	Х	Х	Х		X	Х	Х	

2. Course Outcomes-Program Outcomes (POs) Relationship Matrix (Indicate the relationships by mark "X")

P-Outcomes	а	b	С	d	е	f	g	h	i	J	k	Ι
C-Outcomes												
1	Х	Х	Х	Х	Х			Х	Х	Х	Х	Х
2	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х
3	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х
4	Х	Х	Х							Х	Х	Х
5	Х	Х	Х							Х	Х	Х
6	Х	Х	Х							Х	Х	Х
7	Х	Х	Х							Х	Х	Х

### 3. Courses (with title & code)-Program Outcomes (POs) Relationship Matrix

(Indicate the relationships by mark "X"





P-Outcomes Courses	а	b	С	d	е	f	g	h	i	j	k	I
Electrical Networks Lab	X	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х

# **4. Program Educational Objectives (PEOs)** –**Vision/Mission Matrix** (Indicate therelationships by mark "X")

		Mission of de	partment	
PEOs	Higher Learning	Contemporary Education	Technical knowledge	Research
Graduates will have a successful technical or professional careers, including supportive and leadership roles on multidisciplinary teams	Х	X	Х	X
Graduates will be able to acquire, use and develop skills as required for effective professional practices		X	Х	
Graduates will be able to attain holistic education that is an essential prerequisite for being a responsible member of society	Х		X	
Graduates will be engaged in life- long learning, to remain abreast in their profession and be leaders in our technologically vibrant society.	Х		Х	X

5. Program Educational Objectives(PEOs)-Program Outcomes(POs) Relationship Matrix (Indicate the relationships by m

P- Outcome s PEOs	a	b	с	d	e	f
1	Х	Х	Х	Х	Х	
2	Х	Х	Х	Х	Х	
3		Х	Х	Х		Х
4				Х		





relationships by mark "X")

	Course-Outcomes	1	2	3	4	5	6	7
	Course-Objectives							
/	1	Х	Х	Х	Х	Х	Х	Х
	2	Х	Х	Х	Х	Х	X	Х
	3	Х	Х					
	4				Х	Х		
	5			Х	Х	Х	X	Х
	6			Х	Х	Х	Х	Х
	7	Х		Х	Х	Х	X	

Program Educational Objectives (PEOs)-Course Outcomes Relationship Matrix (Indicate the relationships by mark

P-Objectives(PEO)	1	2	3	4
Course-Outcomes				
1	Х	Х		Х
2	Х	Х		Х
3	Х	Х		Х
4	Х	Х		Х
5	Х	Х		Х
6	Х	Х		Х
7	Х	Х		Х

8. Assignments & Assessments-Program Outcomes (POs) Relationship Matrix (Indicate the relationships by mark "X"

P-Outcomes	a	b	c	d	e	f
Assessments						
1	Х	х		Х		х
2	Х	х	Х			х
3	Х	х	х			X
4	Х	Х	X			x

9. Assignments & Assessments-Program Educational Objectives (PEOs) Relationship Matrix (Indicate the relationships by

P-Objectives (PEOs)	1	2	3	4
Assessments				
1	Х	Х		
2		Х		
3		Х	Х	Х
4		Х		
5		Χ		

Assessment process and Relevant Surveys conducted:



# **1.** Constituencies -Program Outcomes (POs) Relationship Matrix (Indicate the relationships by mark "X").

Constituencies

- 1. Alumni
- 2. Government employers
- 3. Students

P-Qutcomes	a	b	c	d	e	f	G	h	i	j	k	1
Constituencies												
1	Х	Х	Х	Х	Х	Х	Х		Х	Х		Х
2	Х	Х	Х	Х	Х	Х	Х		Х			Х
3	Х	Х			Х	Х	Х	Х		Х	Х	Х



### Department of Electrical & Electronics Engineering

### 9 CO-Cognitive Level Mapping

### Subject : Electrical Networks Lab

CO	Cognitive Learning Level							
	1	2	3	4	5	6		
1		Х						
2			Х					
3						Х		
4				Х				
5		Х						
6			Х					
7		Х						

Cognitive Learning Levels:

CLL1: Remembering

CLL2: Understanding

CLL3: Applying

CLL4: Analyzing

CLL5: Evaluating

CLL6: Creating



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#### **GOKARAJU RANGARAJU** INSTITUTE OF ENGINEERING AND TECHNOLOGY

### Department of Electrical & Electronics Engineering

Dept: .....EEE.....

Sessional Question Paper and Scheme of Evaluation

## **EVALUATION STRATEGY**

Academic Year	: 2018-2019
Academic Year	: 2018-2019

- Semester : I
- Name of the Program: EEE...... B.Tech ...II/I..... Section: A,B

Course/Subject: Electrical Networks Lab..... Code: GR17A2038

Name of the Faculty: Y.Satyavani

Designation: Assistant professor

- 1. TARGET:
- A) Percentage for pass: 100%

#### 2. COURSE PLAN & CONTENT DELIVERY

- PPT presentation of the Lectures
- Solving exercise problems
- Model questions
- 3. METHOD OF EVALUATION
  - 3.1 Daily Attendance
  - $3.2 \square$  Lab records and observation
  - 3.3 
    Mini Projects
  - 3.4 🗆 Viva Voce
  - 3.5 🔲 Internal Examination
  - 3.6 
    Semester/End Examination

4. List out any new topic(s) or any innovation you would like to introduce in teaching the subjects in this Semester.

Signature of HOD



### Department of Electrical & Electronics Engineering

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Feedback from Students



Gokaraju Rangaraju Institute of Engineering & Technology (Autonomous)

Summation of Teacher Appraisal by Student Academic Year 2018-19

Name of the Instructor	Y Satya VANI
Faculty ID	788
Branch	EEE
Class and Semester/Section	11/1/A
Academic Year	2018-19
Subject Title	EN Lab
Total No. of Responses/class strength	51/72

Average rating on a scale of 4 for the responses considered:

S. No	Questions of Feedback	Average
1	How do the teacher explain the subject?	3.2372881355932202
2	The teacher pays attention to	3.1355932203389831
3	The Language and communication skills of the teacher is	3.2033898305084745
4	Is the session Interactive?	3.2881355932203391
5	Rate your teacher's explanation in clearing the doubts	3.2033898305084745
6	Rate your teachers commitment in completing the syllabus	3.1355932203389831
7	Rate your teachers punctuality	3.1864406779661016
8	Para your teachers use of teaching aids	3.1355932203389831
9	Rate your teacher's guidance in other activities like NPTEL, Moodle, Swayam, Projects.	3.2372881355932202
10	What is your overall opinion about the teacher?	3.152542372881356



### Summation of Teacher Appraisal by Student Academic Year 2018-19

Name of the Instructor	Y Satya VANI	
Faculty ID	788	
Branch	EEE	
Class and Semester/Section	II/I/B	
Academic Year	2018-19	
Subject Title	EN Lab	
Total No. of Responses/class strength	48/72	

Average rating on a scale of 4 for the responses considered:

S. No	Questions of Feedback	Average		
1	How do the teacher explain the subject?	3.3559322033898304		
2	The teacher pays attention to	3.4406779661016951		
3	The Language and communication skills of the teacher is	3.3898305084745761		
4	Is the session Interactive?	3.3559322033898304		
5	Rate your teacher's explanation in clearing the doubts	3.4237288135593222		
6	Rate your teachers commitment in completing the syllabus	3.3728813559322033		
7	Rate your teachers punctuality	3.3559322033898304		
8	Rate your teachers use of teaching aids	3.4745762711864407		
9	Rate your teacher's guidance in other activities like NPTEL, Moodle, Swayam, Projects.	3.2203389830508473		
10	What is your overall opinion about the teacher?	3.4915254237288136		

Net Feedback on a scale of 1 to 4: 3.3881355932203392

your



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### Department of Electrical & Electronics Engineering

Course Exit Survey

### RUBRIC

### **OBJECTIVE:** Work effectively with others

### STUDENT OUTCOME: Ability to function in a multi-disciplinary team

S.No.	Student Name	Performance Criteria	Unsatisfactory	Developing	Satisfactor Y	Exemplar y	Scor e
			1	2	3	4	
	R.Madhuri (18245A0218)	Research & Gather Information	Does not collect any information that relates to the topic.	Collects very little information some relates to the topic	Collects some basic Informatio n most relates to the topic.	Collects a great deal of Informati on all relates to the topic.	
		Fulfill team role's	Does not perform any duties of assigned team role.	Performs very little duties.	Performs nearly all duties.	Performs all duties ofassigne d team role.	
		Share Equally	Always relies on others to do the work.	Rarely does the assigned work often needs reminding.	Usually does the assigned work rarely needs reminding.	Always does the assigned work without having to be reminded	
		Listen to other team mates	Is always talkingnever allows anyone else to speak.	Usually doing most of the talking rarely allows others to speak.	Listens, but sometimes talks too much.	Listens and speaks a fair amount.	





2.	Revanth (17241A02B0)	Research & Gather Information	Does not collect any information that relates to the topic.	Collects very little information some relates to the topic	Collects some basic informatio nmost relates to the topic.	Average score Collects a great deal of informati onall relates to the topic.	
		Fulfill team role's	Does not perform any duties of assigned team role.	Performs very little duties.	Performs nearly all duties.	Performs all duties of assigned team role.	
		Share Equally	Always relies on others to do the work.	Rarely does the assigned work often needs reminding.	Usually does the assigned work rarely needs reminding.	Always does the assigned work without having to be reminded	
		Listen to other team mates	Is always talkingnever allows anyone else to speak.	Usually doing most of the talking rarely allows others to speak.	Listens, but sometimes talks too much.	Listens and speaks a fair amount.	



# Department of Electrical & Electronics Engineering

						Average score	
3	R.V.Sai Tarun (17241A02A4)	Research & Gather Information	Does not collect any information that relates to the topic.	Collects very little information some relates to the topic	Collects some basic informatio nmost relates to the topic.	Collects a great deal of informati onall relates to the topic.	
		Fulfill team role's	Does not perform any duties of assigned team role.	Performs very little duties.	Performs nearly all duties.	Performs all duties of assigned team role.	
		Share Equally	Always relies on others to do the work.	Rarely does the assigned work often needs reminding.	Usually does the assigned work rarely needs reminding.	Always does the assigned work without having to be reminded	
		Listen to other team mates	Is always talkingnever allows anyone else to speak.	Usually doing most of the talking rarely allows others to speak.	Listens, but sometimes talks too much.	Listens and speaks a fair amount.	_
						Average score	



Department of Electrical & Electronics Engineering

### **COURSE COMPLETION STATUS**

Academic Year	: 2018-2019	
Semester	: I	
Name of the Program	n: EEE B.Tech	II/I Section: A,B
Course/Subject: Electri	ical Networks Lab	Code: GR17A2038
Name of the Faculty: Y.	Satyavani I	Dept:EEE
Designation: Assistant	professor	

Program	Remarks	No. of Objectives Achieved	No. of Outcomes Achieved
1	1 & 2 programs completed by 18/07/18		
2		2,3, 4	2,4
3	3 & 4 programs completed by 22/07/18		
4		1,3	2,4
5	5 program completed by 26/07/18	1,3	2,4
6	6 program completed by 29/07/18	1,3	2,4
7	7 program completed by 02/08/18	1,3	2,4
8	8 program completed by 16/08/18	1,3	2,4
9	9 program completed by 23/08/18	1,3	2,4
10	10 program completed by 30/08/18	1,3	2,4
11	11 & 12 program completed by 06/09/18	1,2	2,3
12		1,3	2,1,5
13	13 program completed by 13/09/18	1,3	2,1,5
14	14 programs completed by 27/09/18	1,3	2,1,5
15	15 programs completed by 11/10/18	2,3	1,2,3,6, 7

Signature of HOD

Signature of faculty

Date:

Date:

Note: After the completion of each unit mention the number of Objectives & Outcomes Achieved.



## **GUIDELINES TO STUDY THE COURSE/SUBJECT**

Academic Year	: 2018-2019		
Semester	: I		
Name of the Program:	EEE B.Tech	II/I	Section: A,B
Course/Subject: Electrical	Networks Lab	Code: GF	R17A2038
Name of the Faculty: Y.Sat	yavani I	Dept:	EEE
Designation: Assistant pro	fessor		

#### **Course Design and Delivery System (CDD):**

- The Course syllabus is written into number of learning objectives and outcomes.
- These learning objectives and outcomes will be achieved through lectures, assessments, assignments, experiments in the laboratory, projects, seminars, presentations, etc.
- Every student will be given an assessment plan, criteria for assessment, scheme of evaluation and grading method.
- The Learning Process will be carried out through assessments of Knowledge, Skills and Attitude by various methods and the students will be given guidance to refer to the text books, reference books, journals, etc.

The faculty be able to -

- Understand the principles of Learning
- Understand the psychology of students
- Develop instructional objectives for a given topic
- Prepare course, unit and lesson plans
- Understand different methods of teaching and learning
- Use appropriate teaching and learning aids
- Plan and deliver lectures effectively
- Provide feedback to students using various methods of Assessments and tools of Evaluation
- Act as a guide, advisor, counselor, facilitator, motivator and not just as a teacher alone

Signature of HOD Date:

Signature of faculty Date:



Department of Electrical & Electronics Engineering

### ILLUSTRATIVE VERBS FOR STATING INSTRUCTIONAL OBJECTIVES

These verbs can also be used while framing questions for Continuous Assessment Examinations as well as for End – Semester (final)Examinations

Design

ILLUSTRATIVE VERBS FOR STATING GENERAL OBJECTIVES/OUTCOMES

Understand	
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ILLUSTRATIVE VERBS FOR STATING SPECIFIC OBJECTIVES/OUTCOMES:

#### A. COGNITIVE DOMAIN (KNOWLEDGE)

1	2	3	4	5	6
	Comprehension	Application	Analysis		Evaluation
Knowledge	Understanding	of knowledge &	Of whole w .r.t. its	Synthesis	
		comprehension	constituents		Judgment
	-	· · ·		·	
Define	Convert	Demonstrate		Categorize	
			Differentiate	_	Compare
Identify	Describe (a			Combine	_
		Prepare	Discriminate		
	Procedure)	-		Design	
		Relate	Distinguish		
	Distinguish		0	Generate	
	8	Show	Separate		
			•	Plan	
	Explain why/how	Solve			

	FFECTIVE DOMAIN TTITUDE)	C. <u>P</u>	SYCHOMOT	OR DOMAIN (SK	ILLS)	
Assist	Select	Bend	Dissect	Insert	Perform	Straighten
Change	Develop	Calibrate	Draw	Keep	Prepare	Strengthen
		Compress	Extend	Elongate	Remove	Time
		Conduct	Feed	Limit	Replace	Transfer
		Connect	File	Manipulate	Report	Туре
		Convert	Grow	Move Precisely	Reset	Weigh
		Decrease	Increase	Paint	Set	